

# G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL

## M.Tech II – SEMESTER (Scheme-2017)

### FIRST SESSIONAL EXAMINATIONS – APRIL – 2018

**Time : 10:30 A.M. to 12:30 P.M**

Date	Day	Paper No.	Computer Science and Engineering	Structural Engineering	Communications and Signal Processing	VLSI and Embedded Systems	Thermal Sciences and Energy Systems	CAD/CAM	Power Electronics
23-04-2018	MON	1	<b>AI</b> - Artificial Intelligence	<b>ARCD</b> - Advanced Reinforced Concrete Design	<b>DIPPR</b> -Digital Image processing and Pattern Recognition	<b>LVD</b> - Low Power VLSI Design	<b>FEA</b> –Finite Element Analysis	<b>FEA</b> –Finite Element Analysis	<b>SDCD</b> – Solid State DC Drives
24-04-2018	TUE	2	<b>CC</b> – Cloud Computing	<b>ASSD</b> – Advanced Structural Steel Design	<b>OCN</b> -Optical Communication	<b>VDFT</b> - VLSI Design for Testability	<b>AHT</b> – Advanced Heat Transfer	<b>RBT</b> –Robotics	<b>SACD</b> - Solid State AC Drives
25-04-2018	WED	3	<b>DS</b> – Data Science	<b>SS</b> -Stability of Structures	<b>SSP</b> – Statistical Signal Processing	<b>VLSIA</b> -VLSI Architecture	<b>GTT</b> – Gas Turbine Theory	<b>FMS</b> – Flexible Manufacturing Systems	<b>HVDC</b> - HVDC and FACTS
26-04-2018	THU	4	<b>SQT–Elective–III:</b> Software Quality and Testing	<b>SD</b> - Structural Dynamics	<b>MWC</b> – Microwave Communications	<b>MESD</b> – Microcontrollers for Embedded System Design	<b>ECM</b> – Energy Conservation & Management	<b>RD</b> – Robust Design	<b>ASPES</b> -Advance simulation of Power Electronic Systems
27-04-2018	FRI	5	<b>FOSS-Elective IV:</b> Free and open source software	<b>FEM-Elective –III:</b> Finite Element Methods	<b>CR- Elective – III:</b> Cognitive Radio	<b>EDAT-Elective -III:</b> Electronic Design Automation Tools	<b>DHE -Elective–III:</b> Design of Heat Transfer Equipment	<b>SMS- Elective -III:</b> Simulation of Manufacturing Systems	<b>IAPE–Elective–III:</b> Industrial Applications of Power Electronics
28-04-2018	SAT	6	---	<b>ERDS-Elective–IV:</b> Earthquake Resistant Design of Structures	<b>DDPS- Elective – IV:</b> Design of Digital Signal Processing Systems	<b>HSC -Elective – IV:</b> Hardware Software Co-Design	<b>DACS -Elective-IV:</b> Design of Air Conditioning Systems	<b>RPTM- Elective IV:</b> Rapid Prototyping Tooling and Manufacture	<b>RES- Elective – IV:</b> Renewable Energy Sources

**Note: - No grace period for entering into the examination hall during the Sessional and End exams. Students are allowed into halls 15 minutes before commencement of examinations.**

**PRINCIPAL**